

## RTL LEVEL

### RTL SIGN-OFF TOOLS

#### RTL FUNCTIONAL VERIFICATION

Cadence  
AXIOM  
Aldec  
Avery  
Blue Pearl Software  
Maia  
nSys  
Real Intent  
SpringSoft  
YogaTech  
Zocalo

#### FORMAL VERIFICATION

Cadence - Verplex  
Synopsys  
Jasper - Safelogic  
Mentor  
Axiom Design Automation (@HDL)  
Calypto  
OneSpin  
Prover Technology

#### FORMAL ANALYSIS

Mentor - 0-In  
Cadence - Verplex  
Averant (HDAC)  
Atrenta  
Real Intent  
Assertive Design  
Avery  
Axiom Design Automation (@HDL)  
IBM  
Jasper  
Jeda  
NEC  
OneSpin  
Synopsys - InnoLogic Systems  
Veritable

#### DESIGN DEBUG

SpringSoft - Novas Software  
ASIC Analytic  
Atrenta  
Axiom - Protometer  
Jasper  
Mentor  
NextOp  
Real Intent  
Severity1  
Synopsys - Synplicity  
Vennsa  
Veritools

#### HW/SW CO-VERIFICATION

Carbon Design  
Mentor  
Adveda  
Agilent EEsof EDA  
Aldec  
Cadence  
Dolphin Integration  
Endeavor Intertech  
Foresight Systems  
Synopsys  
TNI-Valiosys - Arexsys

#### RTL DESIGN & ENTRY

Cadence  
Mentor  
TransEDA  
Atrenta  
Veritools  
Synopsys  
Aldec  
Ansys - Apache - Sequence  
ARM - Obsidian Software  
ATA  
Concept Engineering  
Dolphin Integration  
EMA  
Excellicon  
Expressive Systems - (Vizef)  
Hantro  
HDL Works  
Interra Systems  
Jasper  
Jeda  
Levetate  
Real Intent  
Sigasi  
SoftJin  
Source III  
Stelar Tools  
SynaptiCAD  
Tanner EDA  
Time-Rover  
Translogic  
VeriEZ  
Verific  
Willamette

### RTL ANALYSIS TOOLS

#### TIMING & SIGNAL-INTEGRITY DESIGN

Synopsys  
Ansys - Ansoft - Pacific Numerix  
Atrenta  
Cadence - CadMOS  
Fujitsu  
Applied Simulation Technology - Contec  
Averant  
Avertec  
Incentia Design Systems  
Mentor  
Real Intent

#### POWER DESIGN

Synopsys  
Atrenta  
Ansys - Apache - Sequence

ASC  
Bulldast  
Calypto  
Library Technologies

Mentor  
TransEDA  
Veritools

#### EMI DESIGN

Ansys - Ansoft  
Applied Simulation Technology - Contec  
Fujitsu  
Lorentz Solutions  
Mentor - Flowmerics  
National Instruments - Electronics Workbench  
Premier EDA Solutions Ltd  
Zuken - Incases

### RTL DESIGN TOOLS

#### MIXED LANGUAGE SIMULATION

Synopsys  
Mentor - Model Tech  
Cadence  
ALDEC  
Dolphin Integration  
Fintronic  
Tachyon

#### VHDL SIMULATION

ALDEC  
Adveda  
ALS Design  
Cadence Dolphin Integration  
Green Mountain  
IK Tech  
Impulse Accelerated Technologies  
Mentor - Innoveda  
MyCAD  
Symphony EDA  
Synopsys - Zycad

#### VERILOG SIMULATION

Fintronic  
Synopsys  
Cadence  
ALDEC  
TransEDA  
Axiom Design Automation (@HDL) - IBM  
Dolphin Integration  
Mentor  
Tachyon (Pragmatic C Software)  
Simucad - Silvaco  
Tau Simulation  
Winterlogic  
Veripool  
Real Intent

#### LOGIC SYNTHESIS

Synopsys  
Mentor - Exemplar  
Cadence - Get2Chip  
Aldec  
ALS Design  
Altium (Protel) - Accolade (Metamore)  
ATA  
Derivation Systems  
FTL Systems  
Incentia Design Systems  
Oasys  
Pacific Design  
SynApps  
Tiempo  
Verific/Softjin

#### MIXED SIGNAL SIMULATION

Mentor  
Synopsys  
Cadence  
Agilent EEsof EDA EEsof EDA  
ALS Design  
Ansys - Ansoft  
Dolphin Integration  
FTL Systems  
Simucad - Silvaco  
uSysintegral  
Zuken

### ACCELERATION, EMULATION, & RAPID PROTOTYPING

#### HARDWARE

Cadence - Quickturn  
Mentor - Meta Systems  
EVE  
Gidel  
Star Bridge Systems

#### RAPID PROTOTYPE

Synopsys  
Dini Group  
Aldec (Alatek)  
ChipIt  
Dynalith Systems  
HiTech Design  
Impulse  
Pittsburgh Simulation  
Polaris Design  
S2C

#### SOFTWARE

Auspy  
Mentor - SpeedGate  
Synopsys - Synplicity

### DESIGN FOR TEST TOOLS

#### DFT TOOLS

Mentor  
Synopsys  
Cadence  
Atrenta  
SynTest Technologies  
Genesys  
Zuken  
Intusoft  
Acugen  
Alpine  
ASC  
Asset Intertech - TI  
Astek

#### ATEEDA

Comit  
DeFacTo Technologies  
Flynn Systems  
Incentia Design Systems  
Intellitech  
iROC Technologies  
Macraigor Systems  
Simucad - Silvaco  
Teseda (Deft)  
Winterlogic

### MISCELLANEOUS

#### DESIGN LIBRARIES

Synopsys  
Agilent EEsof EDA  
Ansys - Ansoft - Compact Software  
Cadence  
Caesium  
Cast  
Hantro  
Intusoft  
Levetate Design Systems  
Library Technologies  
Mentor  
SynaptiCAD  
Tanner EDA  
Zuken - Redac

#### ENTERPRISE & CIS TOOLS

Zuken  
Dassault - Synchronicity  
PTC - Ohio Design Automation  
IC Manage  
Hyperformance  
Oridus  
ClioSoft  
Agilent  
ALS  
Aprisa  
Arena Solutions  
Cadence - Orcad  
Design and Reuse  
Mentor  
OpenIT  
Partminer  
Posegde  
Precience  
Sapient Systems  
Satin  
Translogic  
Xuropa  
YDC

#### INTEROPERABILITY TOOLS

Platform Computing  
Synopsys  
Cadence - Orcad - Team Corp.  
Sybase  
ATA Engineering  
Aldec  
Attune Systems  
Avery  
Clisoft  
DataXpress  
Electronic Tools Co.  
IBM  
Interra Systems  
LSI Logic  
Mentor - Veribest (Intergraph)  
MethodICs  
MicroEDA  
NEC  
Oasis Tooling  
Pinebush Technologies  
Precience - (Element 14 - CADSoft)  
Q Point Technology  
Router Solutions  
Saratoga Data Systems  
Softtech - Workgroup Technologies  
SoftJin  
Source III  
Tanner  
Tuscany  
X-Tec  
Zuken

### GATE LEVEL

#### RF DESIGN & SIMULATION

Agilent EEsof EDA  
Ansys - Ansoft - Eagleware - Elantix  
National Instruments - AWR  
Cadence - Neolinear  
Synopsys  
Ago  
Berkeley Design Automation  
Computer Simulation Technology (CST)  
Helic  
Interactive Products  
Mentor - ZeLand Software  
OEA International  
Optotek  
Simucad - Silvaco  
Sonnet Software (KCC Ltd.)  
Webb Labs

#### GATE LEVEL / TRANSISTOR LEVEL DESIGN

Ansoft  
Cadence - Chip Estimate - Giga Scale  
Concept Engineering  
Silicon Frontline  
Solido Design Automation  
Synopsys - Sandwork Design  
Tanner

#### GATE LEVEL SIMULATION

ALDEC  
ALS Design  
Ansys - Ansoft  
Cadence  
Capilano Computing  
Intusoft  
Mental Automation  
Mentor  
National Instruments - Elect Workbench  
Simucad - Silvaco  
Sophia Systems  
SpringSoft - Novas  
Tanner EDA  
Zuken-Redac - Veda

#### ANALOG SIMULATION

Cadence  
Synopsys - NASSDA  
Ansys - Ansoft  
National Instruments  
YDC  
ALS Design  
Cybereda  
Dolphin Integration  
Intusoft  
Legend Design Technology  
Mentor  
Number One Systems  
Tanner EDA  
Zuken