

## DESIGN LIBRARIES

Synopsys  
Agilent EEsof EDA  
Zuken - Redac  
Ansys - Ansoft -  
Compact Software  
Beach Systems - VCX  
Cadence  
Caesium  
Cast  
Computer Aided Software  
Hantro  
Intusoft  
Levitate Design Systems  
Library Technologies  
Mead  
Mentor  
Project Technology  
SynaptiCAD  
Tanner EDA  
Tenko

## ENTERPRISE & CIS TOOLS

I2 Technologies - Aspect  
Development  
Dessault  
PTC - Ohio Design Automation  
Hyperformance  
Oridus  
IC Manage  
ClioSoft  
Precience  
Agilent EEsof EDA  
ALS  
Aprisa  
Arena Solutions  
Cadence - Orcad  
ChipData  
Cimmetry  
Design and Reuse  
eChips (Chipcenter - Questlink)  
Enovia - MatrixOne - Synchronicity  
IONOS  
Mentor  
Microvision  
Ontic Technology  
OpenIT  
Partminer  
Posedge  
Simerica Logic Semantics  
Translogic  
TransModeling  
YDC  
Zuken

## INTEROPERABILITY TOOLS

Platform Computing  
Cadence  
Sybase  
Attune Systems  
TurboWorx  
Precience  
Zuken  
Aldec  
ATA Engineering  
Avaki  
Avery  
Clisoft  
DataXpress  
Electronic Tools Co.  
Gridware  
IBM  
IBM - Rational  
Interra Systems  
LSI Logic  
Mentor  
MicroEDA  
NEC  
Oasis Tooling  
Pinebush Technologies  
Prosilog  
Q Point Technology  
Router Solutions  
Saratoga Data Systems  
Silicon Navigator  
SmartLynx  
Softtech - Workgroup  
Technologies  
SoftJin  
Source III  
Synopsys  
Tanner  
Tops  
X-Tec

## VERILOG SIMULATION

Fintronic  
Cadence  
Synopsys  
ALDEC  
TransEDA  
Axiom Design Automation  
(@HDL)  
Cogency  
Dolphin Integration  
Mentor  
Pragmatic C Software  
Provis  
Simucad - Silvaco  
Tau Simulation  
Wellspring

## RTL VERIFICATION

Aldec  
Avery  
nSys  
Real Intent (Validity)  
YogaTech  
Zocolo

## FORMAL VERIFICATION

Cadence  
Synopsys  
Jasper - Safelogic  
Axiom Design Automation  
(@HDL)  
Mentor  
Applied Logic Tech  
Formal Verification  
Formalized Design  
Magma  
OneSpin  
Prover Technology

## FORMAL ANALYSIS

Mentor  
Cadence  
Averant (HDAC)  
Real Intent  
Assertive Design  
Atrenta  
Avery  
Axiom Design Automation  
(@HDL)  
IBM  
Jasper  
Jeda  
NEC  
OneSpin  
Synopsys  
Veritable

## SYNTHESIS

Synopsys  
Synplicity  
Magma - Aplus Design  
Technology (ADT)  
Cadence - Get2Chip  
Mentor - Exemplar  
Aldec  
ALS Design  
Altium  
ATA  
BlueSpec  
Derivation Systems  
Epsilon Design Systems  
FTL Systems  
Incentia Design Systems  
ISData  
NTT Data (Harmonix)  
Pacific Design  
SynApps  
Verific/Softjin

## TIMING DESIGN

Synopsys  
Cadence  
Extreme DA  
CLK Design Automation  
Atrenta  
Averant  
Avertec  
EMA - Forte -  
Chronology/Cynapps  
FishTail Design Automation  
IBM  
Incentia Design Systems  
Magma  
Mentor  
Real Intent (Validity)

## SIGNAL-INTEGRITY DESIGN

Synopsys  
Ansys - Ansoft  
Applied Simulation Technology  
- Contec  
Cadence  
Magma  
Mentor  
Quantic

## POWER DESIGN

Synopsys  
Ansys - Ansoft  
Sequence  
ACAD  
ASC  
Atrenta  
BullDAST  
Calypto  
Library Technologies  
Mentor  
Offis  
Summus Design  
TNI-Valiosys-TransEDA  
Veritools

## EMI DESIGN

Ansys - Ansoft  
Applied Simulation Technology  
- Contec  
Mentor - Flowmerics  
NorInvest  
Electronics Workbench  
Lorentz Solutions  
Premier EDA Solutions Ltd  
Zuken - Incases

## MIXED-LANGUAGE SIMULATION

Cadence  
Synopsys  
Mentor  
ALDEC  
Dolphin Integration  
Fintronic

## MIXED-SIGNAL SIMULATION

Mentor  
Cadence  
Synopsys  
Agilent EEsof EDA  
ALS Design  
Ansys - Ansoft  
Dolphin Integration  
FTL Systems  
IBM  
Simucad - Silvaco  
uSysintegral  
Zuken

## DESIGN DEBUG

SpringSoft - Novas Software  
Atrenta  
Axiom - Protometer  
Real Intent  
Synopsys - Synplicity  
Veritools

## RTL DESIGN & ENTRY

Denali  
Mentor  
TransEDA  
Veritools  
EMA - Forte - Chronology/Cynapps  
ALDEC  
ATA  
Atrenta  
Blue Pearl Software  
Cadence  
CADfusion (Singapore)  
Cohesion  
Concept Engineering  
Diagonal Systems  
Dolphin Integration  
Expressive Systems - (Vizef)  
GeneriCAD  
Hantro  
HDL Works  
Icinergy  
Inoru Technologies  
Interra Systems  
Javelin  
Jeda  
Levitate  
Lewis Systems  
Magma  
Obsidian Software  
Orion  
Peritus - Vista Technologies  
R-Active  
Sequence  
SoftJin  
Source III  
SynaptiCAD  
Synopsys  
Tanner EDA  
Time-Rover  
Topdown  
Translogic  
VeriEZ  
Verific  
Verilog  
Visual Software  
Willamette

## LOGIC SYNTHESIS

Synopsys  
Magma - Aplus Design Technology  
(ADT)  
Cadence  
Mentor  
Aldec  
ALS Design  
Altium (Protel) - Accolade  
(Metamore)  
ATA  
BlueSpec  
Derivation Systems  
Epsilon Design Systems  
FTL Systems  
Incentia Design Systems  
ISData  
NTT Data (Harmonix)  
Pacific Design  
SynApps  
Verific/Softjin  
Xilinx

## VHDL SIMULATION

ALDEC  
Adveda  
ALS Design  
Cadence  
Dolphin Integration  
Green Mountain  
IK Tech  
Impulse Accelerated Technologies  
Intellx  
Mentor  
MyCAD  
Symphony EDA  
Synopsys

## HW/SW CO-VERIFICATION

Mentor  
Carbon Design  
Adveda  
Agilent EEsof EDA  
Aldec  
Cadence  
Coverify  
CoWare  
Dolphin Integration  
Endeavor Intertech  
Foresight Systems  
Integrated Intellectual  
Property - Indus  
TNI-Valiosys - Arexsys

## DESIGN TEAM - ACCELERATION & EMULATION

**Hardware**  
Cadence - Verisity - Axis  
EVE  
Mentor  
AppNet  
ForteLink  
GateRocket  
Liga Systems  
Logic Express  
Simpod  
Star Bridge Systems

## Rapid Prototype

Synopsys  
Gidel  
Dini Group  
Aldec (Alatek)  
ChipIt  
Digital Design Solutions  
Dyalith Systems  
Eridon  
Impulse  
Mentor  
Pittsburgh Simulation

## Software

Auspy  
Mentor  
Synopsys - Synplicity

## DFT TOOLS

Mentor  
Synopsys  
Cadence  
SynTest Technologies  
Genesys  
Zuken  
Acugen  
Alpine  
ASC  
Asset Intertech - TI  
Astek  
ATG - Zuken-Redac  
Atrenta  
Comit  
DeFacTo Technologies  
Flynn Systems  
Giordano Auto  
Incentia Design Systems  
Inovys  
Intellitech  
Intellx  
Intusoft  
iROC Technologies  
Macraigor Systems  
Magma  
Provis (Zycad)  
Simucad - Silvaco  
Teseda (Deft)  
Winterlogic

## ANALOG DESIGN

Cadence - Antrim  
AWR  
Accelicon  
Anadigm  
AnalogRails  
Anasift Technology  
AVP  
Berkeley Design  
COMCAD  
Infinscale  
Interactive Data Visualization  
Kimotion  
Lynguent  
Magma - Sabio  
Mephisto Design Automation  
MunEDA  
Orora Design Technologies  
Pochaho  
Silicon Frontline  
Simucad - Silvaco  
Solido Design Automation  
SynCira  
Synopsys - Analog Design  
Automation  
Tanner  
TCI  
Zeni EDA Systems

## SCHEMATIC CAPTURE

Cadence  
Mentor  
JEDAT (Seiko BluWave)  
YDC  
Autodesk  
National Instruments  
Zuken  
Norinvest  
ALS Design  
Altium  
American Small Business  
Capilano  
Cohesion  
Concept Engineering  
Dolphin Integration  
Electronics Workbench  
Elgris Technologies  
IK Tech  
Intercept - ESA Design  
Ivex  
Mental Automation  
Microcode Engineering  
Microsoft Visio  
MyCAD  
Number One Systems  
Phase Three Logic  
Preception Software  
Simucad - Silvaco  
Tanner EDA  
Wacom  
Ziegler Informatics

## GATE LEVEL SIMULATION

National Instruments  
Zuken - Redac - Veda  
ALDEC  
ALS Design  
Ansys - Ansoft  
Cadence  
Capilano Computing  
Electronics Workbench  
IK Tech  
Intusoft  
Mental Automation  
Mentor  
Simucad - Silvaco  
Sophia Systems  
SprinSoft

## ANALOG SIMULATION

Synopsys  
Ansys - Ansoft  
Intusoft  
YDC  
Zuken  
ALS Design  
Dolphin Integration  
EverCAD  
Legend Design Technology  
Mentor  
Number One Systems  
Paragon IC  
Tanner EDA

## RF DESIGN & SIMULATION

Agilent EEsof EDA  
AWR  
Ansys - Ansoft  
Cadence  
BayTech  
Berkeley Design  
Computer Simulation  
Technology (CST)  
Designaid  
Helic  
Interactive Products  
Knowlent  
Ocean Software  
OEA International  
Optotek  
Orion Software  
RHR Labs  
Simucad - Silvaco  
Sonnet Software (KCC Ltd.)  
TSV Engineering  
Waypoint  
Webb Labs  
Zeland Software

## VERIFICATION TEAM ACCELERATION & EMULATION

Cadence  
Mentor  
**GATE LEVEL / TRANSISTOR LEVEL DESIGN**  
Ansift  
Cadence  
Concept Engineering  
Silicon Frontline  
Solido Design Automation  
Synopsys  
Tanner

RTL LEVEL

GATE LEVEL