Gary Smith EDA Activities at DAC

ESL, CAE, CAD/CAM Wallcharts (coming) **and What To See at DAC 2011** (coming) **cards will be available at each event.**

Gary Smith EDA at DAC

Sunday June 5
Time: 7:00-8:00 pm
Location: Omni Hotel, Ballroom Salon AB

Come hear Gary & Mary's annual update on the state of EDA.

3D ICs
Mary Olsson
The current status of the 3D IC market and how it affects EDA.

Responsibilities of the Semiconductor Design Infrastructure
Gary Smith
We are evolving into a new Semiconductor ecosystem. It is imperative that all members of the ecosystem do their part to keep the market healthy. This talk will outline EDA's responsibilities and how to carry them out.

No refreshments will be served. No need to register.
Right next door to EDAC reception.

DAC Pavilion Panel
Gary Smith on EDA: Trends and What's Hot at DAC

Monday June 6
Time: 9:15-10:15 am
Location: Booth #3421

Gary Smith of Gary Smith EDA reviews EDA's hottest technology trends. How will the dramatic changes in EDA, the semiconductor market and the design community affect you? What are the hot 'must sees' at this year's conference? Find out here!

This year's panel will start with some blues music with Gary, Bob Gardner (EDAC) and Mike Santarini (Xilinx). Kick start your Monday at DAC!

What's Hot at DAC 2011

http://www.youtube.com/watch?v=Y4BCHAIe8yQ&feature=related

DAC "Headaches" Panel

Monday June 6
Time: 3:30-4:30 pm
Location: DAC meeting room 25A

A technical panel at DAC about 1) IP reuse in SoC designs
and 2) variation in custom IC designs.

Two recent surveys found that "variation" was the #1 headache for
custom IC design and "IP reuse" was the #2 headache for SoC design.

Moderator:

  - John Cooley, DeepChip.com founder

Panelists:

 - Gary Smith, EDA industry analyst
 - David Genzer, Dir of IC design, Biotronik
 - Jim Hogan, EDA investor
 - Trent McConaghy, CSO, Solido Design
 - Shiv Sikand, VP Eng, IC Manage

Link: http://www.deepchip.com/wiretap/110509.html