

SYSTEM DESIGN AUTOMATION

SDA Design & Simulation

Agnisys • EDaptive • IBM - Telelogic - iLogix • MathWorks • Mentor • Mirabilis • MSC Software

ELECTRONIC SYSTEM LEVEL

ARCHITECT'S WORKBENCH

NETWORK SIMULATION

BEHAVIORAL DESIGN

MathWorks
Intel - CoFluent
Concurrent EDA
Docea
IBM - Telelogic - iLogix
Jasper
MathWorks

Mentor
Mirabilis
MLDesign
ProximusDA
Space Codesign
Synopsys

ArchSite
Synopsys - CoWare

ESL HARDWARE

ESL Design & Entry

Synopsys
Mentor
Intel - CoFluent
Atrenta
Actis
Agilent
Agnisys
Aldec
Bellum
Cadence
Codetronix
Duolog
Easics
Expressive Systems
Handshake Solutions
CA Technologies - Hyperformance (SES)
InterDesign Technologies
MataiTech
Mirabilis
National Instruments
Poseidon Design Systems
Prosilog
S2C
Sankhya Technologies

High Level Synthesis (HLS)

Forte - Chronology/Cynapps
Calypto - Mentor
Bluespec
Synopsys - Synfora
ACE - Compaan Design
Algotochip
Axilica
Cadence
Concurrent EDA
Impulse Accelerated Technologies
MathWorks
NEC
Space Codesign
SystemCrafter
Target Compiler Technologies
Xilinx - AutoESL
Y Explorations

SoC Integration Platform

Atrenta
Duolog - Beach
Cadence - Chip Estimate
Duolog
Magillem
Mataitech
Parallel Engines
Sapient-IC

Silicon Virtual Prototype

Atrenta
Chip Path
ENTASYS
Magillem
Mentor
ProximusDA
Xilinx

ESL Simulation

Synopsys
Aldec
ARM
Bluespec
Carbon
MathWorks
Mentor
Mesquite Software
VWorks
Xilinx

ESL Target Compiler

Sonics
Agnisys
ARRIS
Arteris
Atrenta
Cadence - Denali
Carbon - ARM
Codasip
Duolog
iNoCs
Inventure
Memoir
Mentor
Mirabilis
NISC
PDTi (Productivity Design Tools)
Semifore
Silistix
Xilinx

Application Engine Compiler

BINACHIP
Cadence - Tensilica
Critical Blue
Mirabilis
Poseidon
Synopsys
Target Compiler Technologies

ESL Power Analysis

Ansys - Apache - Sequence
Mentor
Atrenta
DOCEA
Entasys
Mirabilis

ESL Thermal Analysis

DOCEA

Accelerator Library & Development Tools

Forte - Arithmatica

ES Level RF Design

Agilent EEs of EDA
National Instruments - AWR
Ansys - Ansoft - Simec

ARCHITECTURAL DESIGN

ESL SOFTWARE

Parallel Programming

ACE - Compaan
Connective Logic
Cray
CriticalBlue
Gedae
IBM
Intel - RapidMind
MathWorks
MBA Sciences
NVIDIA
Oracle - Sun
Proximus
Rocketick
Sankhya
Silicon Hive
Vector Fabrics

ESL Compiler

ACE
CodePlay
Target Compiler Technologies

Algorithm Design & Optimization

Synopsys
Forte
Critical Blue
Sigmatix

Model Development Tools

Carbon
Mentor
Agnisys
ARM - Axys
Bluespec
Embedded Systems Environment
Infiniscale
Prosilog
Synopsys - VaST
Target Compiler Technologies
VWorks

Software Synthesis

Agnisys
M2B
Mathworks
Vayavya Labs (Vlabs)
Verum

Multicore Development Tools

ARM
Mentor
3L, Ltd
Allinea Software
American Arium
Axiom - SysChip
B Labs
Cavium - Monta Vista
Codeplay Software
Critical Blue
Elbrus
ENEA
eXludas
Rocketick
Express Logic
First Silicon Solutions
Green Hills
Intel
Intelliasys
klocwork
Mathworks
National Instruments
Nohau Elektronik
Plurality
PolyCore Software
QNX Software Systems
RapidMind
RoweBits Research
Segger Micro
Stream Processors
Synopsys - Virage - ARC International
TenAsys
TotalView Tech

Software Virtual Prototype

Synopsys
Intel - Virtutech
Imperas
ARM
Cadence
Carbon
Mentor
Qemu
Space Codesign
Vworks (ASTC)
Xilinx

ESL VERIFICATION

Intelligent Test Bench

Mentor
Breker
Cadence - Verisity - Inspec
Synopsys - Springsoft - Certess
Zocalo Tech
Agnisys

ESL Test & Verification

Cadence - Verisity - Inspec
Agnisys
Avery
Duolog - Beach Systems
Diagonal Systems (DS)
IBM
JEDA
Levitate Design Systems
Magillem
MataiTech
Mentor

Semifore
Source III
Synopsys - Springsoft - Novas
Structural Design Verification
Synapticad
Temento
TransEDA
Xilinx

Transaction-Based Acceleration & Emulation

Cadence
Mentor
Synopsys - EVE
Aldec
Tarek

ESL Formal Verification

Calypto
Jasper
MathWorks