

# WHAT TO SEE @ DAC 2013

COMPANY	CATEGORY	BOOTH
Defacto	DFT	409
Ausdia	RTL Sign-Off Tools	633
OneSpin	RTL Sign-Off Tools	846
Sonics	ESL NoC Compiler	921
ARM	Manycore Embedded Tools	931
Synopsys	Transaction-Based A&E	947
Real Intent	RTL Sign-Off Tools	1031
IC Manage	Design Debug	1041
Oasys	Logic Synthesis	1231
Calypto	High Level Synthesis	1247
Invarian	RTL Sign-Off Thermal	1332
Apache	RTL Sign-Off Thermal	1346
Library Technology	Low Power Library Development	1443
ProPlus	DFY	1525
Blue Pearl Software	RTL Sign-Off for FPGAs	1533
Agnisys	Software Synthesis	1543
Forte	High Level Synthesis	1547
SemiFore	ESL Target Compiler	1831
Concept Engineering	Design Debug	1842
Atrenta	Silicon Virtual Prototype	1847
Solido	Transistor Level Design	1915
Vayavya	Software Synthesis	1929
Breker	Intelligent Test Bench	2015
Berkeley Design Automation	SPICE	2025
AtopTech	ASIC Layout	2033
Mentor	Transaction-Based A&E	2046
Docea	ESL Thermal	2113
Cadence	Transaction-Based A&E	2214
Aldec	Design Debug	2225
Sage Design	DRC	2233
Duolog	Silicon Virtual Prototype	2246
Jasper	RTL Sign-Off Tools	2346
JEDAT	IC Custom Layout	2427
Tanner EDA	Analog Design	2442

[www.garysmithEDA.com](http://www.garysmithEDA.com)

contact Gary Smith [gary@garysmithEDA.com](mailto:gary@garysmithEDA.com) [mary@garysmithEDA.com](mailto:mary@garysmithEDA.com)

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