

ELECTRONIC SYSTEM LEVEL

BEHAVIORAL DESIGN

+ Architect's Workbench

MathWorks
Intel - CoFluent
Cadence - Jasper
Concurrent EDA
COSIDE
Docea
IBM - Telelogic - iLogix
Mentor - Summit
Mirabilis
MLDesign
ProximusDA
Space Codesign
Synopsys - Cadis

+ Network Simulation

ArchSile
Synopsys - CoWare

ESL HARDWARE

+ ESL Design & Entry

Synopsys - CoWare - LisaTek
Mentor
Intel - CoFluent
Actis
Agnisys
Aldec
ARM - Duolog
Atrenta
Bellum
CA Technologies - Hyperformance (SES)
Cadence
Codetronix
COSIDE
Easics
Expressive Systems
InterDesign Technologies
Keysight EEs of EDA
MataiTech
Mirabilis
National Instruments
Poseidon Design Systems
Prosilog
S2C
Sankhya Technologies

+ High Level Synthesis (HLS)

Cadence - Forte - Chronology/Cynapps - Calypto - Mentor
Bluespec
Synopsys - Target Compiler Technologies
ACE - Compaan Design
Aldec
Algotochip
Axilica
Concurrent EDA
Impulse Accelerated Technologies
MathWorks
NEC
Space Codesign
SystemCrafter
Xilinx
Y Explorations

+ SoC Integration Platform

Atrenta
Cadence - Chip Estimate
ARM - Duolog
Magellim
Mataitech
Parallel Engines
Sapient-IC

+ Silicon Virtual Prototype

Atrenta
Chip Path
ENTASYS
Magillem
Mentor
ProximusDA
Xilinx

+ ESL Power Analysis

Mentor
Ansys - Apache - Sequence
Atrenta
DOCEA
Entasys
Mirabilis

ARCHITECTURAL DESIGN

+ ESL Simulation

Cadence
Synopsys
Aldec
ARM
ASTC/VLAB
Bluespec
Carbon Design
COSIDE
Imperas
MathWorks
Mentor
Mesquite Software
Xilinx

+ ESL Target Compiler

Agnisys
ARM - Duolog
Atrenta
Cadence - Denali
Carbon Design
Codasip
iNoCs
Inventure
Memoir
Mentor
Mirabilis
NISC
PDTi (Productivity Design Tools)
Semifore
Sillistix
System RDL

+ Application Engine Compiler

BINACHIP
Cadence - Tensilica
Critical Blue
Mirabilis
Poseidon
Synopsys - CoWare - LISATek
Sankhya

+ ESL Thermal Analysis

DOCEA

+ Accelerator Library & Development Tools

Cadence - Forte - Arithmatica

+ ES Level RF Design

Keysight EEs of EDA
National Instruments - AWR
Ansys - Ansoft - Simec
COSIDE

+ Platform IP

ARM - Sunrise Micro Devices
Imagination - MIPS
Sonics
Arteris
Cadence - Tensilica
CEVA
Synopsys

+ Parallel Programming

ACE - Compaan
Connective Logic
Cray
CriticalBlue
Gedae
IBM
Intel - CiLK Arts
MathWorks
MBA Sciences
NVIDIA
Oracle - Sun
Proximus
Rocketick
Sankhya
Silicon Hive
Vector Fabrics

+ ESL Compiler

ACE
CodePlay
Synopsys - Target Compiler Technologies

+ Algorithm Design & Optimization

Synopsys
Cadence - Forte
COSIDE
Critical Blue
Sigmatix

+ Model Development Tools

Carbon Design - Axys
Mentor - SpiraTech
Agnisys
ASTC/VLAB
Bluespec
CircuitSutra
COSIDE
Embedded Systems Environment
Imperas
Infiniscale
Intel
Prosilog
Synopsys - Target Compiler Technologies

+ Software Synthesis

Agnisys
COSIDE
M2B
Mathworks
Vayavya Labs (Vlabs)
Verum

ESL SOFTWARE

+ Embedded Software Development Tools

ARM - Keil
Mentor - CodeSourcery
3L, Ltd
Allinea Software
American Arium
AMIQ
Argon
ASSET InterTech - Arium
Axiom - SysChip
B Labs
Cadence
Cavium - Monta Vista
Codeplay Software
Critical Blue
Elbrus
ENE
eXludas
Express Logic
First Silicon Solutions
Green Hills
Imperas
Intel
Intelliasys
Klocwork
Lauterbach
MathWorks
National Instruments
Nohau Elektronik
Plurality
PolyCore Software
QNX Software Systems
RapidMind
RoweBits Research
SECO
Segger Micro
Stream Processors
Symtavision
TenAsys
TotalView Tech

+ Software IP

ARM - Wicentric
Broadcom
Mentor
Mirabilis Design
Synopsys - Coverity

+ Software Virtual Prototype

Synopsys
Intel - Virtutech
Imperas
Mentor
ARM
ASTC/VLAB
Cadence
Carbon Design
COSIDE
Qemu
Space Codesign
Xilinx
Virtual Devive Technologies

+ Other Software

Synopsys
Roadwave
Imperas

ESL VERIFICATION

+ Intelligent Test Bench

Mentor - Lighthouse (Silicon Forest)
Agnisys
Breker
Cadence - Verisity - Inspec
COSIDE
Synopsys - Springsoft - Certess
Zocalo Tech

+ ESL Test & Verification

Magillem
Agnisys
Avery
Cadence - Verisity - Inspec
COSIDE
Diagonal Systems (DS)
ARM - Duolog - Beach Systems
IBM
JEDA
Levetate Design Systems
MataiTech
Mentor
Semifore
Source III
Structural Design Verification
Synapticad
Synopsys - Springsoft - Novas
Temento
Xilinx

+ Transaction-based

Acceleration & Emulation
Cadence
Mentor
Synopsys- EVE
Aldec
Tarek

+ ESL Formal Verification

Mentor
Cadence - Jasper
Calypto
COSIDE
MathWorks
OneSpin