ELECTRONIC SYSTEM LEVEL

Design Libraries
Synopsys
Keysight EEsof
ANSYS
Cadence
Caesium
Cast
Hantro
Intusoft
Levetate Design Systems
Library Technologies
Mentor Graphics (Siemens)
SynaptiCAD
Zuken - YDC

Silicon IP (Designware)
ARM
Synopsys

CEVA
Sibridge
Adapt-IP

Enterprise & CIS Tools
Zuken - YDC
Altium
IC Manage
Dassault - Synchronicity
PTC - Ohio Design Automation
Oridus
ClioSoft
ALS
Aprisa
Arena Solutions
Cadence - Orcad
Design and Reuse
Dorado
Hyperformance

Mentor Graphics (Siemens)
Missing Link Tools
OpeniT
Partminer
Posedge
Precience
Sapient Systems Satin
SiCAD
Translogic Univa
Xuropa
Zentera

Interoperability Tools
Synopsys
Cadence - Orcad - Team Corp.
Sybase
ATA Engineering
Aldec

Attune Systems
Avery
Cliosoft
DataXpress
Elgris Technologies
IBM - Rational
Interra Systems
Invionics
LSI Logic
Mentor - Tanner EDA
MethodICs
MicroEDA
NEC
Oasis Tooling Pinebush Technologies
Precience - (Element 14 - CADSoft)
Q Point Technology
Router Solutions
Saratoga Data Systems
Softech - Workgroup Technologies

SoftJin
Source III
Tuscany
X-Tec
Zuken

EDA Middleware
Runtime Design Automation
Softjin
Spatial
Verific

RT LEVEL

Formal Verification
Cadence
Synopsys - Magma
OneSpin
Mentor Graphics (Siemens) - Calypto
Prover Technology

Formal Analysis
Mentor Graphics (Siemens) - Axiom
Synopsys - Atrenta
Real Intent
Cadence - Verplex
Averant (HDAC)
OneSpin
Assertive Design
Avery
JEDA Technologies
NEC
Veritable

RTL Design & Entry
Synopsys
Cadence
TransEDA
Synopsys - Atrenta
Mentor Graphics (Siemens) - Tanner EDA
Veritools
Verific
Aldec
ANSYS
ARM - Obsidian Software
ATA
Ausdia
Blue Pearl Software
Concept Engineering
Dolphin Integration
EMA - Chronology/Cynapps
Excellicon
Expressive Systems - (Vizef)
FishTail
Fractal
Hantro
HDL Works
Interra Systems
JEDA Technologies
Levetate
OneSpin
Real Intent
Sigasi
SoftJin
Source III
Stelar Tools
SynaptiCAD - ASC
Time-Rover
Translogic
VeriEZ
Willamette
Elgris Technologies

Design Debug
Aldec
ASIC Analytic
Cadence - Jasper
Concept Engineering
Mentor Graphics (Siemens)
NextOp
Real Intent
Severity
Synopsys - Virage - Atrenta
Vennsa
Veritools

HW/SW Co-Verification
Carbon Design
Mentor Graphics (Siemens)
Adveda
Keysight EEsof
Aldec
Cadence - Verisity - Axis
Dolphin Integration
Endeavor Intertech
Foresight Systems
Synopsys - CoWare
TNI-Valiosys - Arexsys

VHDL Simulation
Aldec
Adveda
ALS Design
Cadence
Dolphin Integration
Green Mountain
IK Tech
Impulse Accelerated Technologies
Mentor Graphics (Siemens) - Innoveda
MyCAD
Symphony EDA
Synopsys - Zycad

Rapid Prototyping
Synopsys - Synplicity - Hardi
The Dini Group
Aldec (Alatek)
Bluespec
Cadence
ChipIt
Dynalith Systems
Gidel
HAPS
HiperSilicon
HiTech Design
HOENS
Impulse
Polaris Design
Reflex
RPP
S2C

Verilog Simulation
Fintronic
Synopsys
Cadence - Verisity - Axis
Aldec
Dolphin Integration
Mentor Graphics (Siemens) - Innoveda
Real Intent
Silvaco
Tachyon (Pragmatic C Software)
Tau Simulation
Veripool
Winterlogic

Mixed-signal Simulation
Mentor Graphics (Siemens)
Cadence
ANSYS
Dolphin Integration
FTL Systems
Keysight EEsof
ProPlus Design Solutions
Silvaco

Logic Synthesis
Synopsys
Cadence - Forte
Mentor Graphics (Siemens) - Oasys
Aldec
ALS Design
Altium
ATA
Derivation Systems
FTL Systems
Incentia Design Systems
Pacific Design
SynApps
Tiberon DA
Tiempo

Verification IP
Arrow Devices
Cadence
Kozio
Mentor Graphics (Siemens)
Sibridge
SmartDV
Synopsys
Test and Verification Solutions
Truechip Solutions

Mixed-language Simulation
Synopsys
Cadence
Mentor Graphics (Siemens)
Aldec
AXIOM
Dolphin Integration
Fintronic
Tachyon (Pragmatic C Software)

Timing & Signal Integrity Analysis
ANSYS
Synopsys - Atrenta
Cadence - Verplex - Verisity
CLK Design
Applied Simulation Technology - Contec
Ausdia
Averant
Avertec
FishTail
Fujitsu
Incentia Design Systems
Keysight EEsof
Mentor Graphics (Siemens)
Real Intent

Power Analysis
Synopsys - Atrenta
ANSYS
Mentor Graphics (Siemens) - Innoveda - Calypto
ASC
BullDAST
Keysight EEsof
Library Technologies
Veritools

EMI Analysis
Applied Simulation Technology - Contec
ANSYS
Cadence
Fujitsu
Lorentz Solutions
Keysight EEsof
Mentor Graphics (Siemens) - Flomerics
National Instruments
Premier EDA Solutions Ltd
Zuken - Incases

DFT Tools
Mentor Graphics (Siemens)
Synopsys - Viewlogic - Atrenta
Cadence
SynTest Technologies
Acugen
Alpine
ASC
Asset Intertech - TI
Astek
ATEEDA
Comit
DeFacTo Technologies
Dolphin - Genesys
Flynn Systems
Incentia Design Systems
Intellitech
Intusoft
iROC Technologies
Macraigor Systems
Silvaco
Teseda (Deft)
Winterlogic
Zuken

GATE LEVEL

RF Design & Simulation
Keysight EEsof
ANSYS
National Instruments
Cadence - Neolinear
Synopsys
Silvaco
AgO
Dassault Systemes - CST
Helic
Interactive Products
Mentor Graphics (Siemens)
OEA International
Optotek
Sonnet Software (KCC Ltd.)
Webb Labs

Gate Level / Transistor Level Design
ANSYS
Cadence - Chip Estimate - Giga Scale
Concept Engineering
Mentor Graphics (Siemens) - Tanner EDA
ProPlus Design Solutions
Silicon Frontline

Solido Design Automation
Synopsys - Sandwork Design

Gate Level Simulation
ALDEC
ALS Design
ANSYS
Cadence
Capilano Computing
Intusoft
Mental Automation
Mentor Graphics (Siemens)
National Instruments
ProPlus Design Solutions
Silvaco
Sophia Systems
Synopsys - Springsoft - Novas
Zuken

Analog Design
Cadence - Antrim
National Instruments
ANSYS
AgO

Anadigm
AnalogRails
Anasift Technology
Asygn
AVP
G-Analog
Infiniscale
Keysight EEsof
Lynguent
Mentor Graphics (Siemens) - Tanner EDA
Mephisto Design Automation
MunEDA
Nostral
Orora Design Technologies
ProPlus Design Solutions
Silicon Frontline
Silvaco
Solido Design Automation
SynCira
Synopsys - Analog Design Automation
Zeni EDA Systems

Analog Simulation
Synopsys
Cadence
ANSYS
National Instruments
Zuken - YDC
ALS Design
Cybereda
Dolphin Integration
G-Analog
Intusoft
Legend Design Technology
Mentor Graphics (Siemens)
Number One Systems
ProPlus Design Solutions